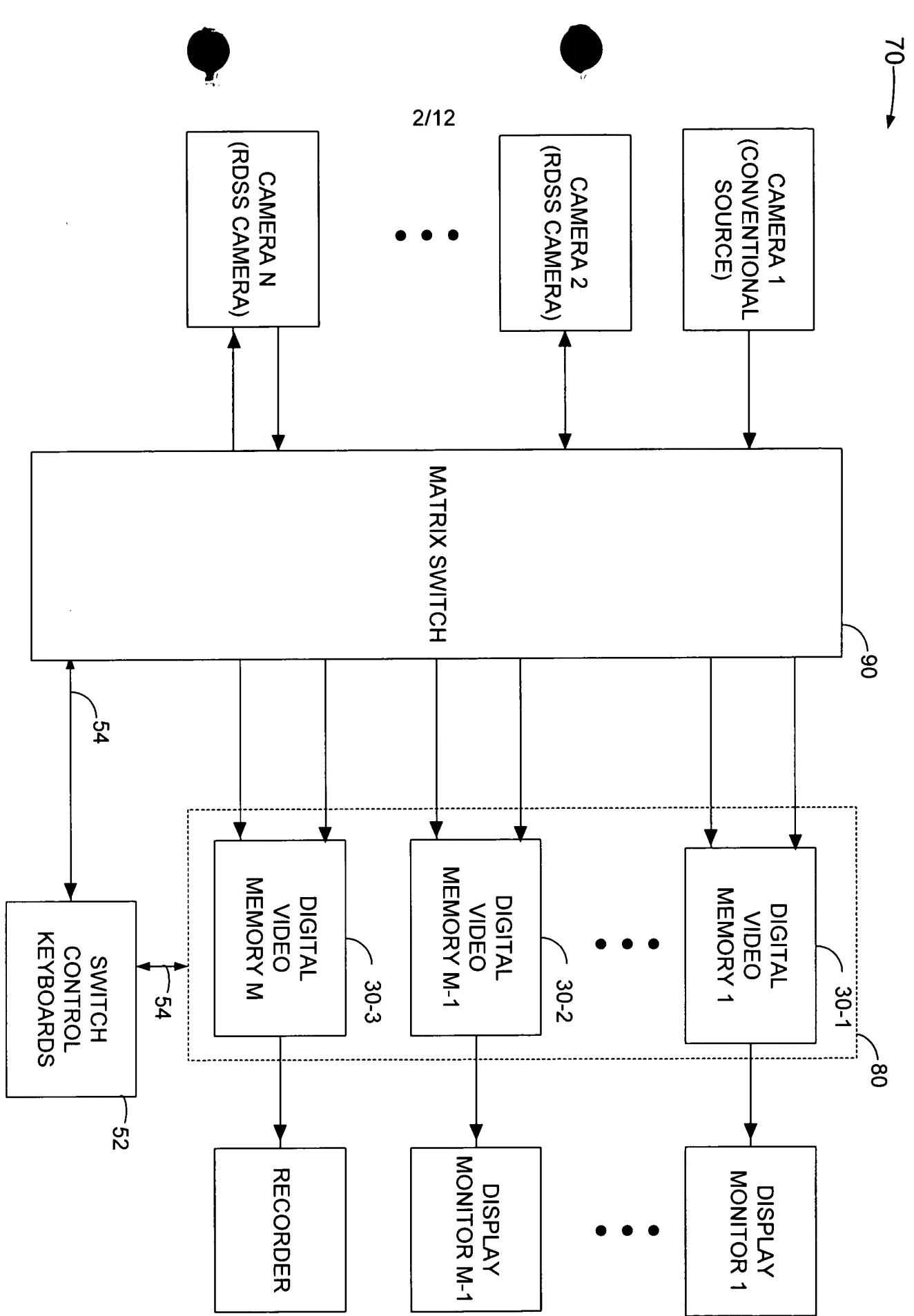
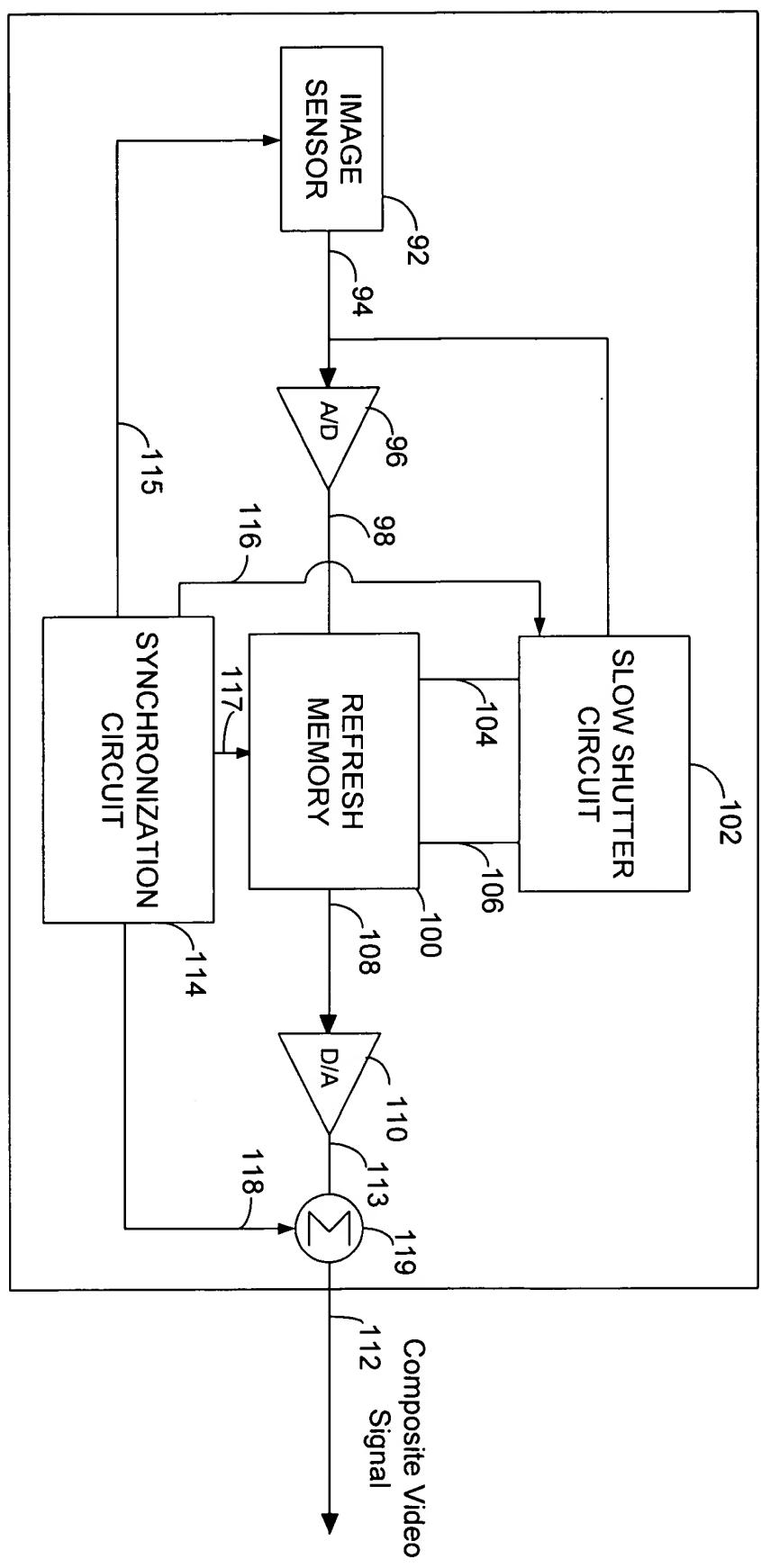
**FIG. 1**



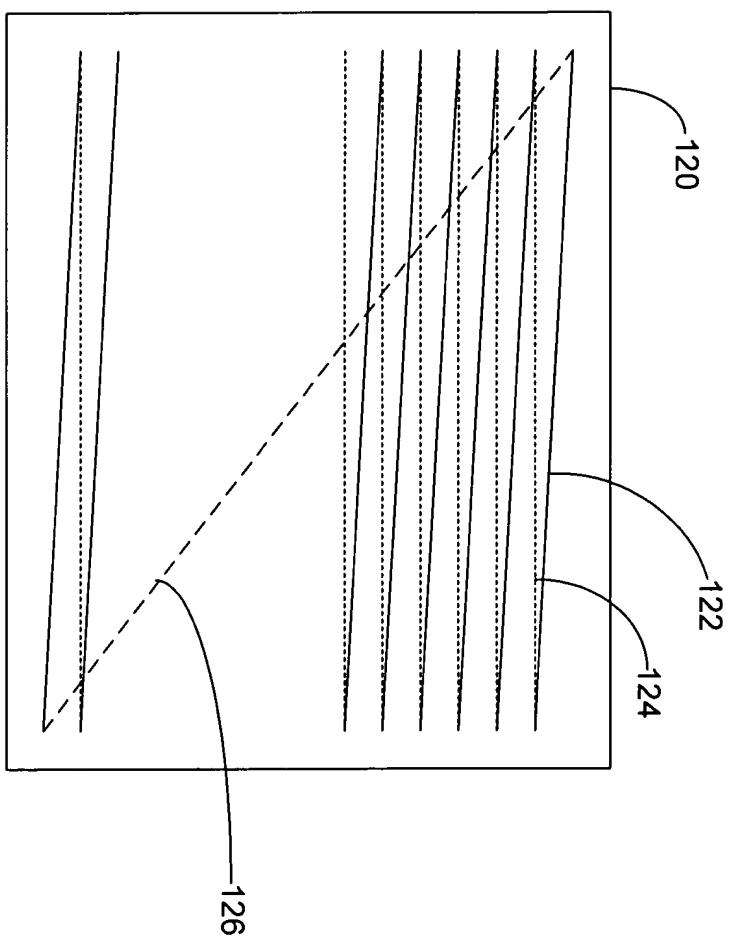
**FIG. 2**

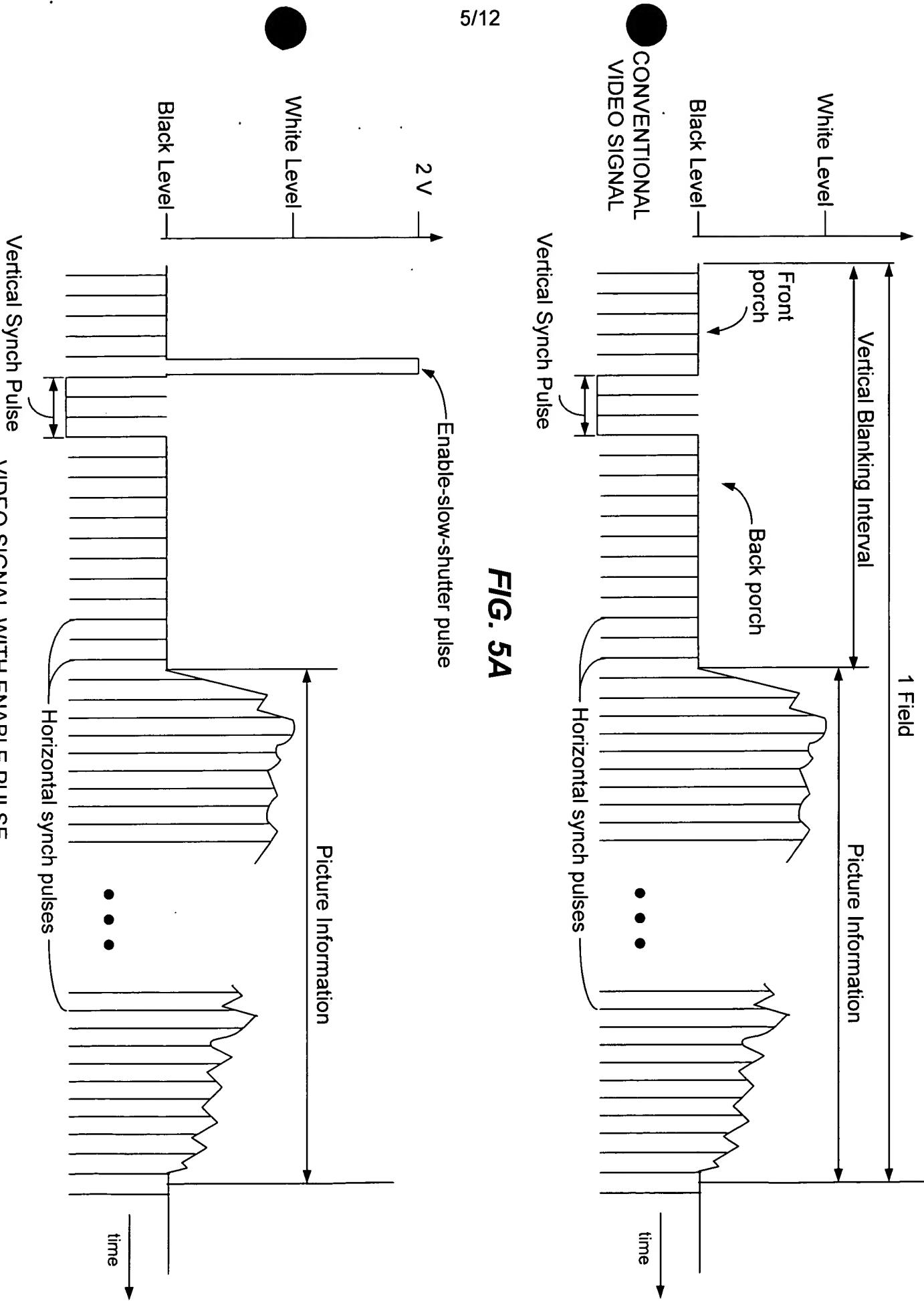


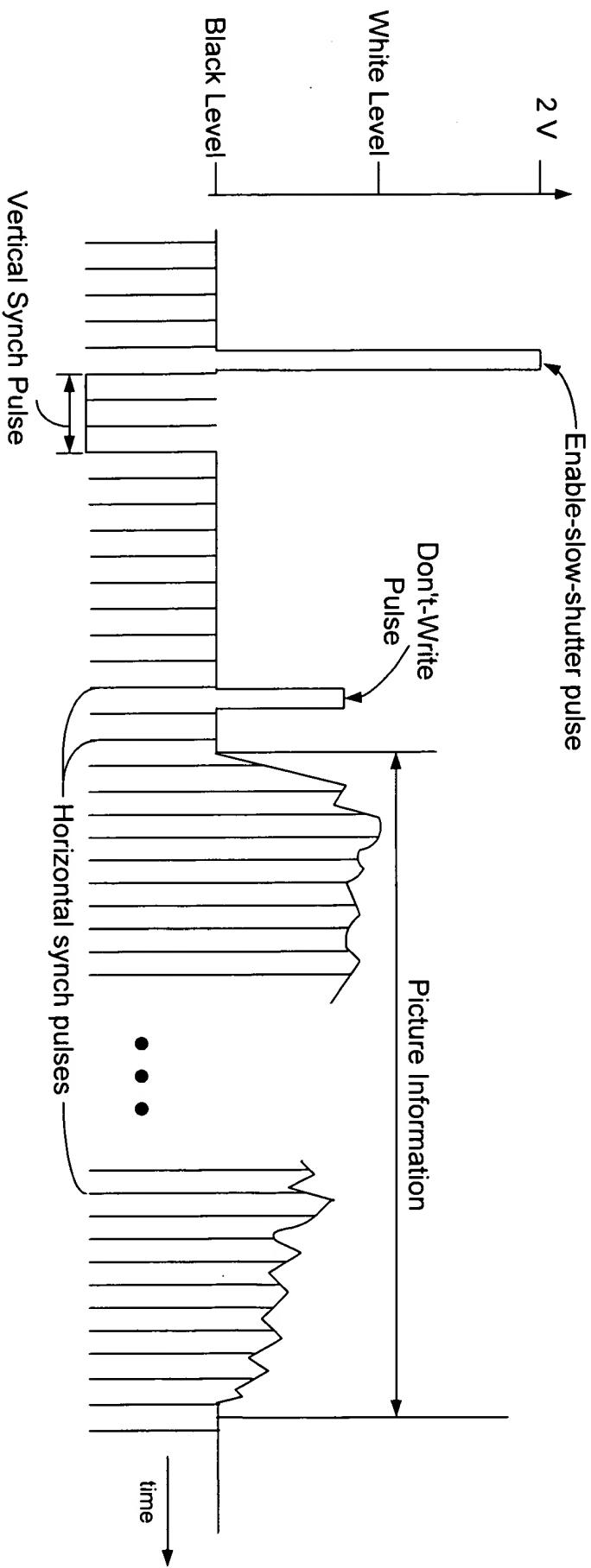
CAMERA

**FIG. 3**PRIOR ART

FIG. 4







VIDEO SIGNAL WITH DON'T-WRITE PULSE

**FIG. 5C**

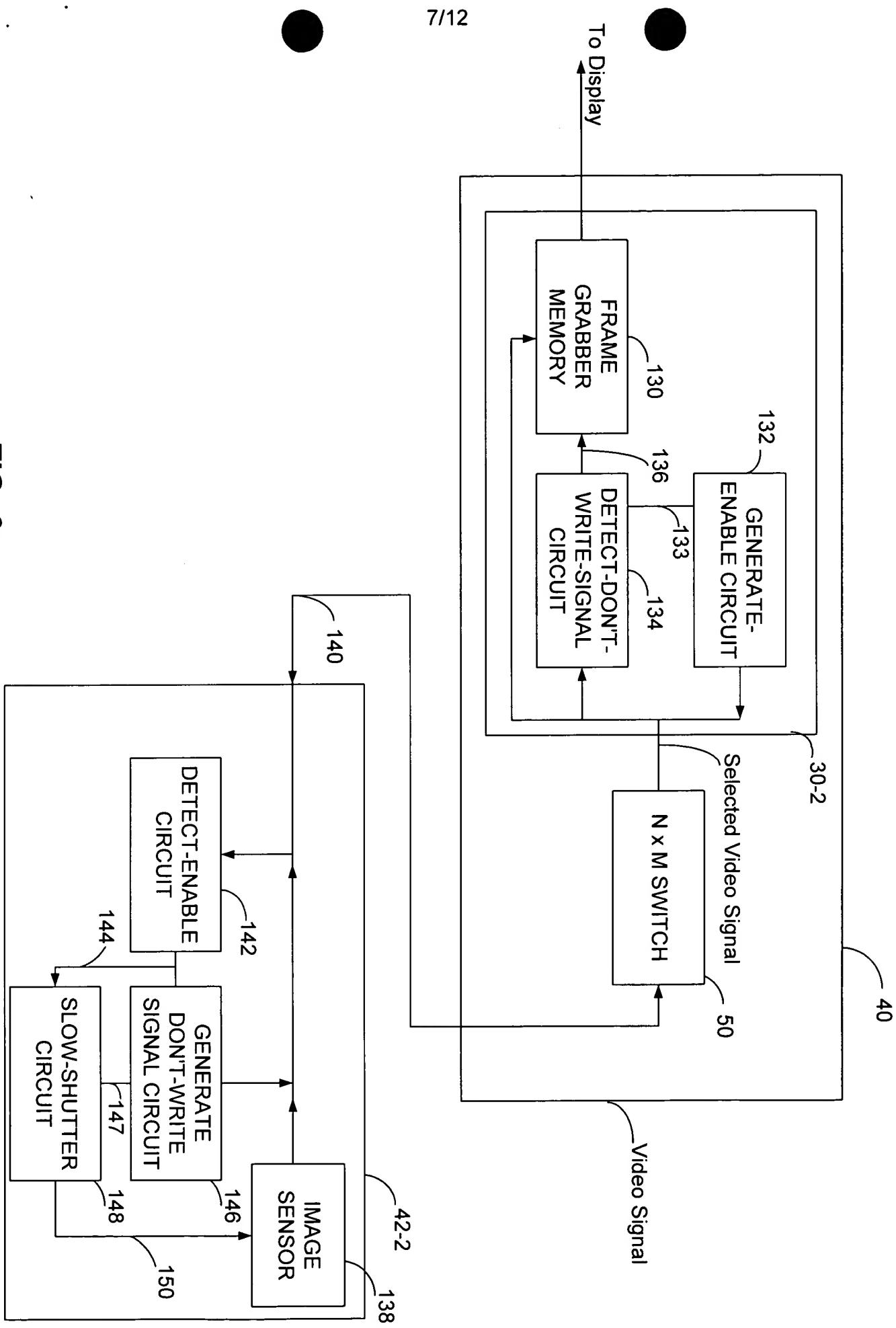


FIG. 6 U.S. Pat. No. 5,800,000

8/12

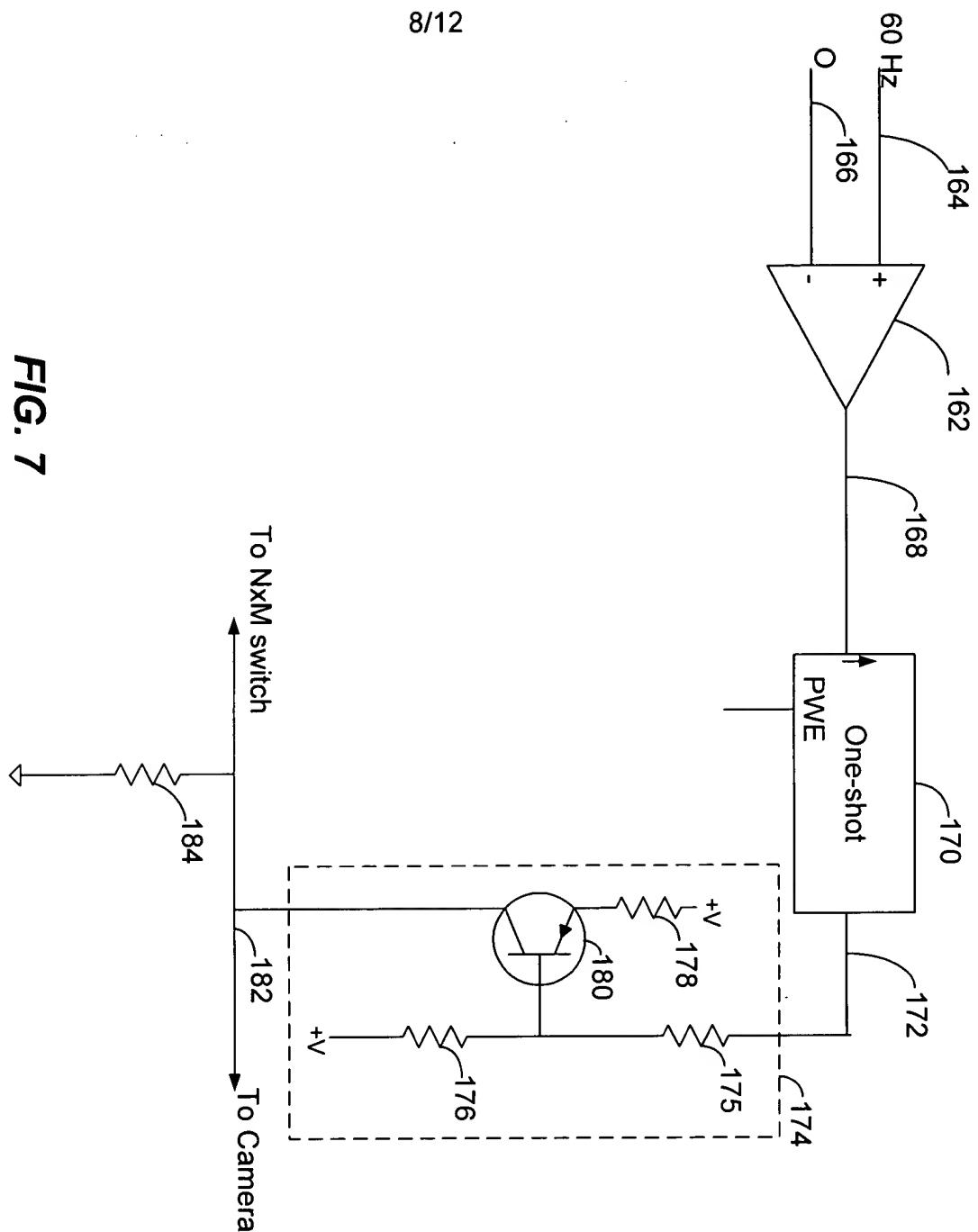
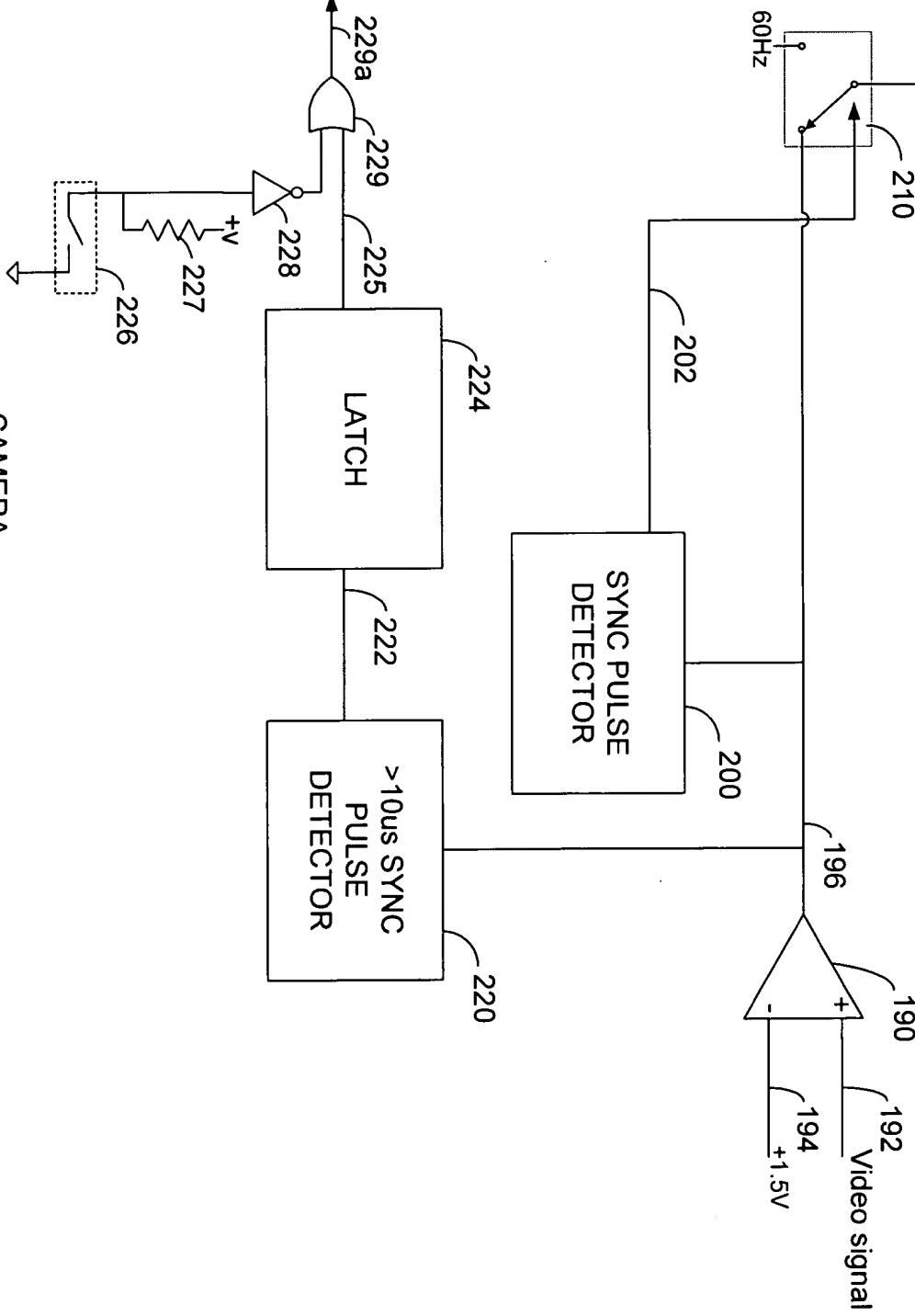
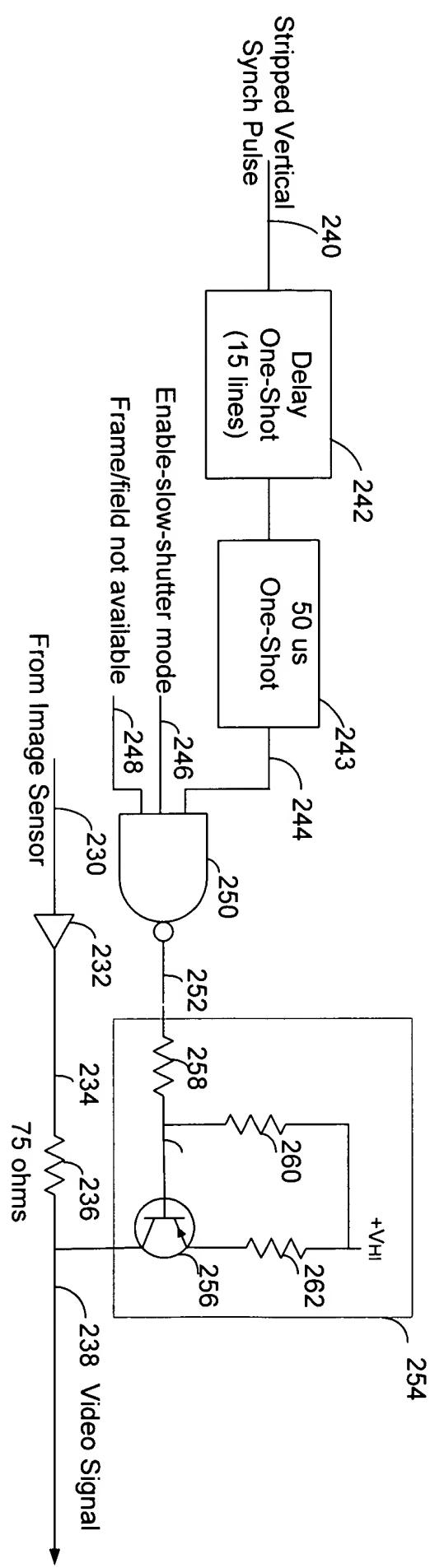


FIG. 7

**CAMERA  
SYNCHRONIZER LOCK  
CIRCUITS**

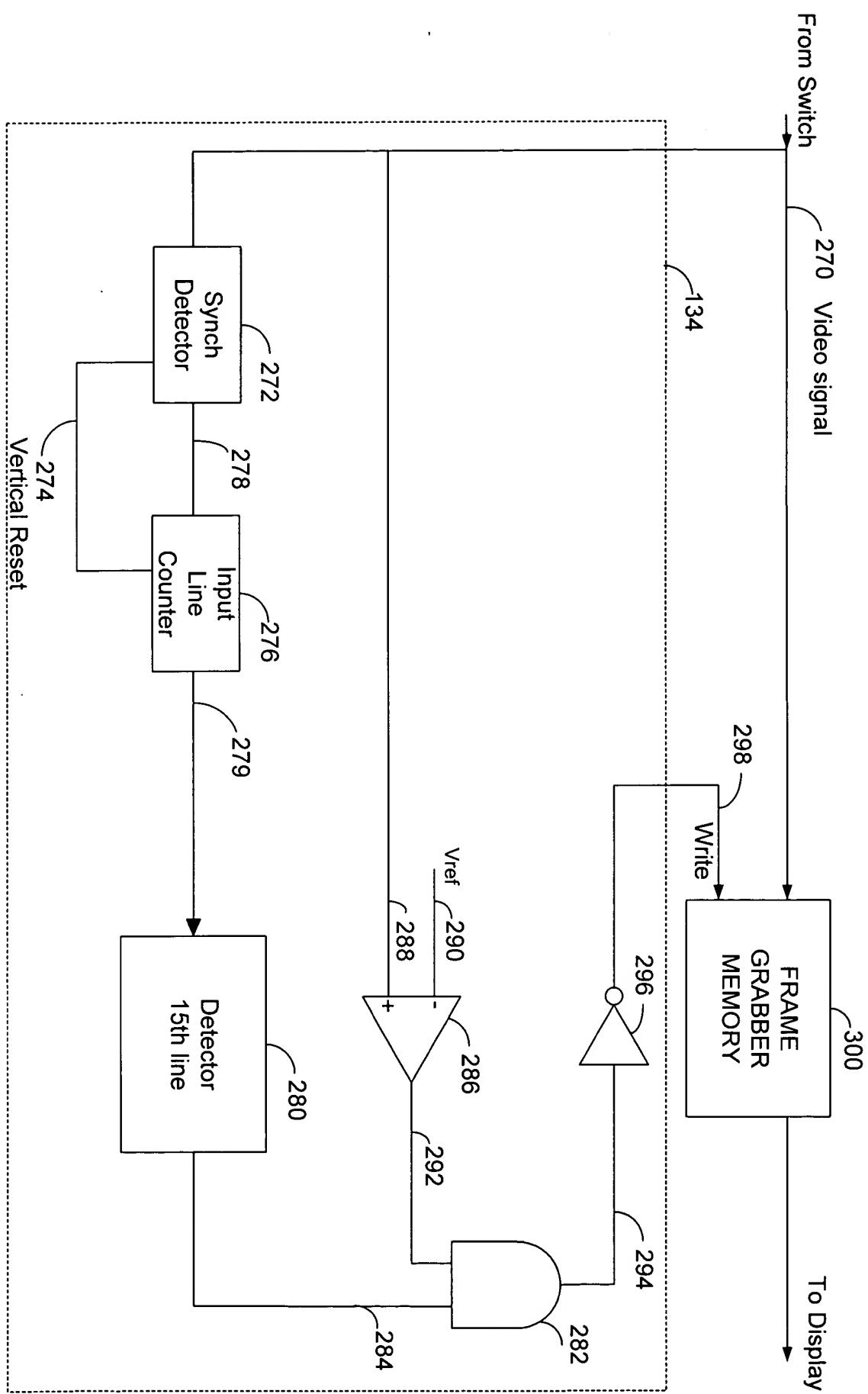


146



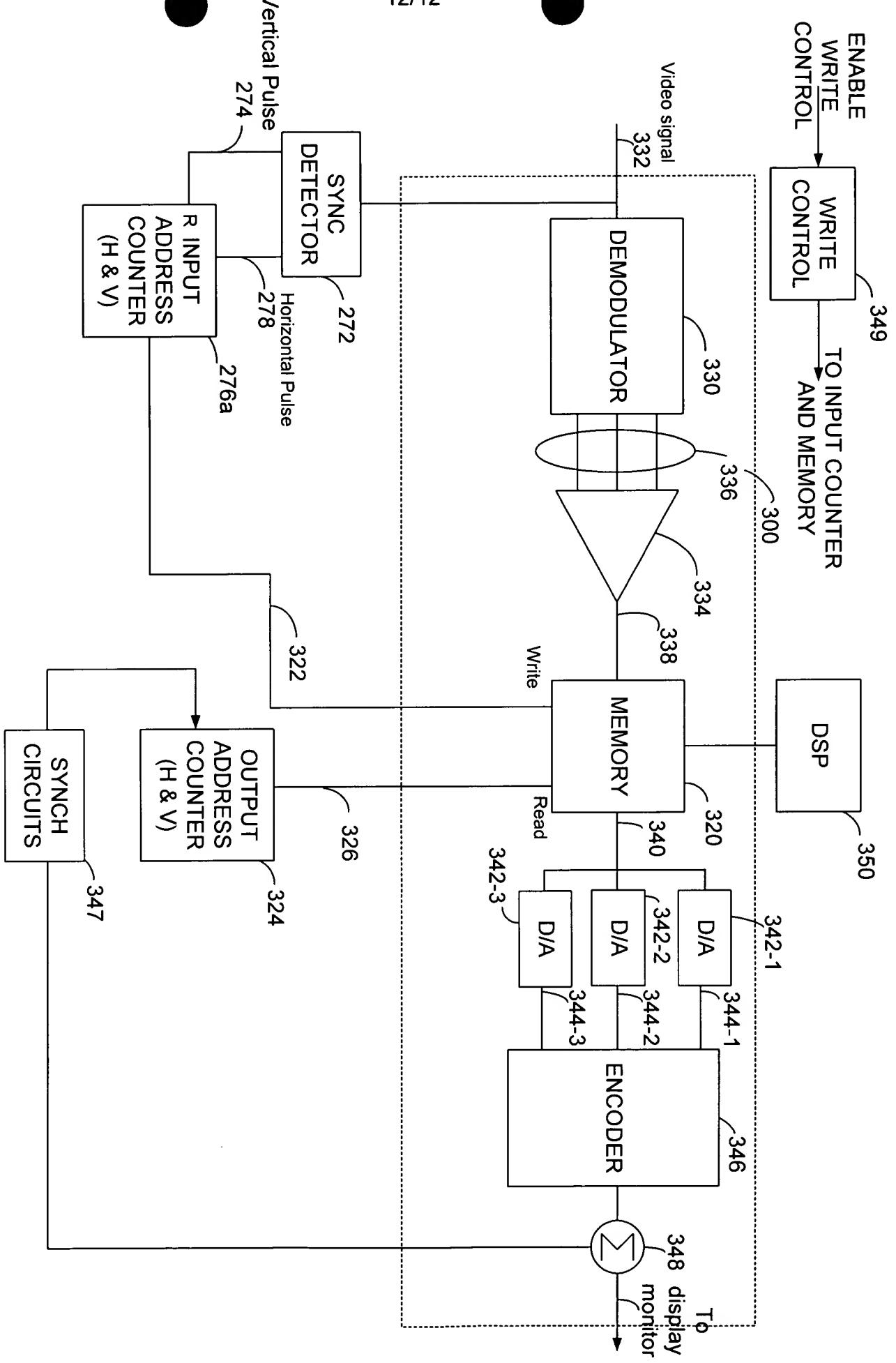
GENERATE DON'T WRITE SIGNAL CIRCUIT

**FIG. 9**



DIGITAL VIDEO MEMORY WITH THE DETECT-DON'T-WRITE SIGNAL CIRCUIT

**FIG. 10**



**FIG. 11**

095534.77 090300